

CLAIMS

- 1    1. A permalloy sensor device having high sensitivity, comprising:
  - 2       a substrate and a sensor on said substrate, said sensor having a first
  - 3       surface, said first surface having a wafer level anisotropy in a given
  - 4       direction; and
- 5       a permalloy resistor pattern of individual runners deposited on
- 6       said first surface such that the mechanical length of each of said
- 7       individual runners is perpendicular to the wafer level anisotropy to cause
- 8       said sensor to have an anisotropy of about 90°.
- 1    2. The device of claim 1, wherein said permalloy is deposited as a
- 2       thin film.
- 1    3. The device of claim 2, wherein said substrate is a silicon wafer.
- 1    4. A permalloy sensor having high sensitivity, comprising:
  - 2       substrate means for forming the body of a sensor and having a first
  - 3       surface, said first surface having a wafer level anisotropy in a given
  - 4       direction; and
- 5       permalloy resistor pattern means for providing individual runners
- 6       deposited on said surface such that the mechanical length of each of said
- 7       individual runners is perpendicular to the wafer level anisotropy to cause
- 8       said sensor to have an anisotropy of about 90°.
- 1    5. The of claim 4, wherein said permalloy is deposited as a thin film.

1    6.    The of claim 5, wherein said substrate is a silicon wafer.

1    7.    A method of forming a permalloy sensor including the steps of:  
2         providing a substrate and a sensor on said substrate, said sensor  
3         having a first surface, said first surface having a wafer level  
4         anisotropy in a given direction; and  
5             depositing a permalloy resistor pattern of individual runners on  
6         said surface such that the mechanical length of each of said individual  
7         runners is perpendicular to the wafer level anisotropy to cause said  
8         sensor to have an anisotropy of about 90°.

1    8.    The method of claim 7, wherein said permalloy is deposited as a  
2         thin film.

1    9.    The method of claim 8, wherein said substrate is a silicon wafer.